

PTO/SB/30 (5/2000)

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U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000,
provides for continued examination of an utility or plant application
filed on or after June 8, 1995.
See The American Inventors Protection Act of 1999 (AIPA).

Application Number	09/452,691
Filing Date	12/02/1999
First Named Inventor	BAILEY
Group Art Unit	2815
Examiner Name	Fenty
Attorney Docket Number	T4-27935

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53 (d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and provisional Application Practice, Interim Rule, 65 Fed. Reg. 14865, (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

1 Submission required under 37 C.F.R. § 1.114

a ☐ Previously submitted

- i. ☐ Consider the amendments(s)/reply under 37 C.F.R. § 1.116 previously filed on _____
(Any unentered amendment(s) referred to above will be entered).
- ii. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
- iii. ☐ Other _____

b ☒ Enclosed

- i. ☒ Amendment/Reply
- ii. ☐ Affidavit(s)/Declaration(s)
- iii. ☐ Information Disclosure Statement (IDS)
- iv. ☐ Other _____

2 Miscellaneous

a ☐ Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103 (c) for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17 (i) required)

b ☐ Other _____

3 Fees


The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.
The Director is hereby authorized to charge the following fees, or credit any overpayments, to
Deposit Account No. 20-0888, Texas Instruments Incorporated.

- a ☒ RCE fee required under 37 C.F.R. § 1.17(e)
- ii. ☒ Extension of time fee (37 C.F.R. §§ 1.136 and 1.17)
- iii. ☐ Other _____

b ☐ Check in the amount of \$ _____ enclosed


c ☐ Payment by credit card (Form PTO-2038 enclosed)

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

Name (Print / Type)	Jacqueline J. Garner	Registration No. (Attorney / Agent)	36,144
Signature		Date	11/3/2003

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box RCE, PO Box 1450, Alexandria, VA 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office on:

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Signature	
Date	11/3/2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/452,691
Applicant: Fred D. Bailey et al.
Filed: 12/02/1999
TCLAU: 2815
Examiner: Jesse A. Fenty

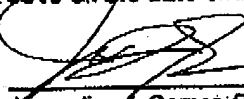
Confirmation No. 1638

OFFICIAL

Docket No.: TI-27935
Customer No.: 23494
Title: AN INTEGRATED CIRCUIT HAVING A THIN FILM RESISTOR LOCATED WITHIN
A MULTILEVEL DIELECTRIC BETWEEN AN UPPER AND LOWER METAL INTERCONNECT LAYER

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November 3, 2003
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT (8 Pages)
<input type="checkbox"/> NEW APPLICATION	<input checked="" type="checkbox"/> EOT 1st (3 Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL (# Pages)
<input type="checkbox"/> FORMAL DRAWINGS	<input type="checkbox"/> ISSUE FEE (# Pages)
<input type="checkbox"/> INFORMAL DRAWINGS	<input type="checkbox"/> REPLY BRIEF (IN TRIPLICATE) (# Pages)
<input type="checkbox"/> CONTINUATION APPN (# Pages)	<input checked="" type="checkbox"/> RCE
<input type="checkbox"/> DIVISIONAL APPN	
NAME OF INVENTOR(S): Bailey	
TITLE OF INVENTION: An Integrated Circuit Having	
TI FILE NO.: TI-27935	DEPOSIT ACCT. NO.: 20-0668
RECEIPT DATE & SERIAL NO.: Serial No.: 09/452,691 Filing Date: 12/02/1999	
FAXED: 11/3/2003 DUE: 10/2/2003 ATTY/SECY: JJS	

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